

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	541	703/14.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L2	335	703/13.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L3	103	703/17.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L4	66	703/20.ccor.	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L5	3	(("6052524") or ("6263302") or ("6604067")).PN.	US-PGPUB; USPAT	OR	OFF	2006/01/06 15:03
L6	64517	memory near4 configur\$6	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L7	38219	(embed\$4 asic) and simulat\$4	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L8	3164	L6 and L7	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L9	2850	L8 and access	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L10	1936	L9 and space	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L11	1449	L10 and read and write	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L12	423	L11 and statistic\$3	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L13	405	L12 and design	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L14	366	L13 and (criteria specification)	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L15	345	L14 and program	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L16	253	L15 and usage	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03
L17	109	L16 and @ad<="20000609"	US-PGPUB; USPAT	OR	ON	2006/01/06 15:03

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12.	((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space) and design [All Sources(- All Sciences -)]	210
11.	((((((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read) and write) and access) and space [All Sources(- All Sciences -)]	221
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8.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) and read [All Sources(- All Sciences -)]	535
7.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic!) and (criteria or specification)) [All Sources(- All Sciences -)]	1028
6.	((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration) and statistic! [All Sources(- All Sciences -)]	1412
5.	((pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!)) and configuration [All Sources(- All Sciences -)]	2799
4.	pub-date > 1959 and pub-date < 2001 and FULL-TEXT((embed! or asic) and memory) and FULL-TEXT(simulat!) [All Sources(- All Sciences -)]	6428
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2.	((((pub-date > 1959 and pub-date < 2001 and FULL-TEXT(memory configuration)) and statistic!) and simulat!) and access [All Sources(- All Sciences -)]	26
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<u>#3</u>	(( ( memory configur*<in>metadata ) <and> ( (on-chip<or>embed<or>asic)<in>metadata ) )) <and> (pyr >= 1951 <and> pyr <= 2000))	16

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**1 High-level library mapping for memories**

Pradip K. Jha, Nikil D. Dutt

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Publisher: ACM Press

Full text available: [pdf\(209.38 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**2 Trap-driven simulation with Tapeworm II**

Richard Uhlig, David Nagle, Trevor Mudge, Stuart Sechrist

November 1994 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the sixth international conference on Architectural support for programming languages and operating systems ASPLOS-VI**, Volume 29 , 28 Issue 11 , 5

Publisher: ACM Press

Full text available: [pdf\(1.45 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**3 A characterization of ten rasterization techniques**

N. Gharachorloo, S. Gupta, R. F. Sproull, I. E. Sutherland

July 1989 **ACM SIGGRAPH Computer Graphics , Proceedings of the 16th annual conference on Computer graphics and interactive techniques SIGGRAPH '89**, Volume 23 Issue 3

Publisher: ACM Press

Full text available: [pdf\(5.15 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**4 Accommodating memory latency in a low-cost rasterizer**

Bruce Anderson, Andy Stewart, Rob MacAulay, Turner Whitted

August 1997 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware**

Publisher: ACM Press

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Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Publisher: ACM Press

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review

**6 Memory modeling for system synthesis**

Sari L. Coumeri, Donald E. Thomas

August 1998 **Proceedings of the 1998 international symposium on Low power electronics and design**

Publisher: ACM Press

Full text available: [pdf\(810.71 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**7 Evaluating the memory overhead required for COMA architectures**

T. Joe, J. L. Hennessy

April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture ISCA '94**, Volume 22 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: [pdf\(1.31 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**8 Introducing memory into the switch elements of multiprocessor interconnection networks**



H. E. Mizrahi, J. L. Baer, E. D. Lazowska, J. Zahorjan

April 1989

**ACM SIGARCH Computer Architecture News , Proceedings of the 16th annual international symposium on Computer architecture ISCA '89, Volume 17 Issue 3**

Publisher: ACM Press

Full text available: pdf(1.15 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**9 Co-design of interleaved memory systems**

Hua Lin, Wayne Wolf

May 2000

**Proceedings of the eighth international workshop on Hardware/software codesign**

Publisher: ACM Press

Full text available: pdf(91.44 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)**10 Architecture and implementation of a VLIW supercomputer**Robert P. Colwell, W. Eric Hall, Chandra S. Joshi, David B. Papworth, Paul K. Rodman, James E. Tornes  
November 1990 **Proceedings of the 1990 ACM/IEEE conference on Supercomputing**

Publisher: IEEE Computer Society

Full text available: pdf(1.29 MB)

Additional Information: [full citation](#), [abstract](#), [references](#)**11 Energy recovery for the design of high-speed, low-power static RAMs**

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June 1997

**ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1997 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '97, Volume 25 Issue 1**

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B. Fleisch, G. Popek

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Full text available: pdf(1.63 MB)

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David Cohen, Ming T. Liu

September 1974 **Conference record of the 7th annual workshop on Microprogramming**

Publisher: ACM Press

Full text available: pdf(430.27 KB)

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Paul J. Sadowski, S. A. Schuster

August 1978 **ACM SIGMOD Record , ACM SIGIR Forum , ACM SIGARCH Computer Architecture News, Volume 10 , 13 , 7 Issue 1 , 2 , 2**

Publisher: ACM Press

Full text available: pdf(1.02 MB)

Additional Information: [full citation](#), [abstract](#), [references](#)**16 Application-controlled physical memory using external page-cache management**

Kieran Harty, David R. Cheriton

September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V, Volume 27 Issue 9**

Publisher: ACM Press

Full text available: pdf(1.40 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**17 A multiple stream microprocessor prototype system: AMP-1**

Edward S. Davidson

May 1980 **Proceedings of the 7th annual symposium on Computer Architecture**

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Full text available: pdf(698.01 KB)

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